

DETAILED ACTION

1. This office action is in response to applicant's response filed on 07/14/2010.
2. Claims 1-6, 8-12 and 14-23 are pending.
3. Claims 7 and 13 are cancelled.
4. Applicant's arguments have been fully considered and they are persuasive.

Therefore the rejections of claims have been withdrawn.

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with attorney Thomas G. Eschweiler on July 15, 2010.

Examiner Amendment to the First page of Specification:

This application claims priority to and the benefit of U.S. Provisional Patent Application Serial No. 60/517,639, which was filed November 5, 2003, entitled

NETWORK INTERFACE SYSTEMS AND METHODS FOR OFFLOADING
SEGMENTATION AND/OR CHECKSUMMING WITH SECURITY PROCESSING.

This application is related to U.S. Patent Application Serial No. 10/676,758, filed on October 1, 2003, entitled PERIPHERAL DEVICES AND METHODS FOR TRANSFERRING INCOMING DATA STATUS ENTRIES FROM A PERIPHERAL TO A HOST, U.S. Patent Application Serial No. 10/676,759, filed on October 1, 2003, entitled DESCRIPTOR MANAGEMENT SYSTEMS AND METHODS FOR TRANSFERRING DATA BETWEEN A HOST AND A PERIPHERAL, U.S. Patent ~~Application~~ Serial No. 7,533,154, filed on February 4, 2004, attorney docket number H1231, entitled DESCRIPTOR MANAGEMENT SYSTEMS AND METHODS FOR TRANSFERRING DATA OF MULTIPLE PRIORITIES BETWEEN A HOST AND A NETWORK, and U.S. Patent ~~Application~~ Serial No. 7,685,434, attorney docket number H 1248, filed on March 2, 2004, entitled TWO PARALLEL_ENGINES FOR HIGH SPEED TRANSMIT IPSEC PROCESSING, the entirety of these applications being hereby incorporated by reference as if fully set forth herein.

Allowable Subject Matter

1. Claims 1-6, 8-12 and 14-23 are allowed.
2. Independent claims 1 and 15 are the inclusion of the following limitations that are not found in the prior art and they are uniquely distinct features. The closest prior arts

are Yang et al. Patent Number 7,003,118 B1 and Cheng et al., Patent No.: US 6,418,130 B1.

Yang discloses architecture for a high performance IPSEC accelerator. The architecture includes components for scanning fields of packets, programming an IPSEC services device according to the scanned fields, and modifying the scanned packet with an output from the IPSEC security services device. Preferably, the architecture is implemented in hardware, and attached to a host machine. Hardware devices, fast in comparison to software processing and network speeds, allows the computationally intensive IPSEC processes to be completed in real-time and reduce or eliminate bottlenecks in the path of a packet being sent or received to/from a network.

And Cheng discloses in a radio telecommunication system, the performance of a mobile unit can be significantly improved during a hand-over procedure by reusing existing security associations that correspond to the mobile unit. By reusing existing security associations, a mobile unit can begin secure communications immediately following the hand-over. Otherwise, and in accordance with conventional practice, the mobile unit will have to undertake the time consuming task of renegotiating the required security associations, before it can begin transmitting and receiving secure communications.

However, these arts, fail to anticipate or render the following limitations: "A network interface system for interfacing a host system with a network to provide outgoing data from the host system to the network and to provide incoming data from the network to the host system, the network interface system comprising:

a bus interface system adapted to be coupled with a host bus in the host system and transfer data between the network interface system and the host system;

a media access control system adapted to be coupled with the network and to transfer data between the network interface system and the network;

a memory system coupled with the bus interface system and the media access control system, the memory system being adapted to store incoming and outgoing data being transferred between the network and the host system;

a security system coupled with the memory system, the security system being adapted to selectively encrypt outgoing data and to selectively decrypt incoming data; and

a descriptor management system coupled with the bus interface system and the security system, the descriptor management system being adapted to obtain initialization vector information from the host system and to provide the initialization vector information to the security system;

wherein the security system is adapted to employ an initial random data string from the outgoing data to begin encryption before security association information has been retrieved by the security system” as claimed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to YONAS BAYOU whose telephone number is (571)272-7610. The examiner can normally be reached on m-f,7:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kambiz Zand can be reached on 571-272-3811. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Yonas Bayou/

Examiner, Art Unit 2434

07/14/2010

/Kambiz Zand/

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Supervisory Patent Examiner, Art Unit 2434